

EE8331: Advanced Analog Integrated Circuit Design

HW #1
Due Friday 01/28/00

- 1) Calculate the minimum capacitance necessary for a 8, 10, 12, 14, 16, 18 and 20 bit system. Plot C_{min} vs number of bits of resolution. Assume a 2.5V supply.
- 2) For strong inversion and saturation the drain to source current is given by

$$I_{ds} = \frac{kW}{2L} (V_{gs} - V_t)^2 (1 + \lambda V_{ds})$$

Likewise for weak inversion and “saturation” the drain to source current is given by

$$I_{ds} = I_{do} \frac{W}{L} e^{\frac{V_{gs}}{nUT}} (1 + \lambda V_{ds})$$

Find a “fitting” function for moderate inversion. The fitting function should be continuous in I_{ds} , g_m and g_{ds}